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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/622,672	07/18/2003	Rajakrishnan Radjassamy	200210141-2	3649
22879	7590	12/27/2006	EXAMINER	
HEWLETT PACKARD COMPANY P O BOX 272400, 3404 E. HARMONY ROAD INTELLECTUAL PROPERTY ADMINISTRATION FORT COLLINS, CO 80527-2400			TRAN, KHANH C	
			ART UNIT	PAPER NUMBER
			2611	
SHORTENED STATUTORY PERIOD OF RESPONSE		MAIL DATE	DELIVERY MODE	
3 MONTHS		12/27/2006	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

Office Action Summary	Application No.	Applicant(s)	
	10/622,672	RADJASSAMY, RAJAKRISHNAN	
	Examiner	Art Unit	
	Khanh Tran	2611	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 18 July 2003.
 2a) This action is FINAL. 2b) This action is non-final.
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 1-24 is/are pending in the application.
 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
 5) Claim(s) _____ is/are allowed.
 6) Claim(s) 1,2,9,10 and 17 is/are rejected.
 7) Claim(s) 3-8,11-16 and 18-24 is/are objected to.
 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.
 10) The drawing(s) filed on 18 July 2003 is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) Notice of References Cited (PTO-892)
 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
 3) Information Disclosure Statement(s) (PTO/SB/08)
 Paper No(s)/Mail Date _____.
 4) Interview Summary (PTO-413)
 Paper No(s)/Mail Date _____.
 5) Notice of Informal Patent Application
 6) Other: _____.

DETAILED ACTION

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

1. Claims 1-2, 9-10 and 17 are rejected under 35 U.S.C. 103(a) as being unpatentable over Hutchison, IV et al. U.S. Patent 6,839,570 B2 in view of Shin U.S. Patent 5,930,527.

Regarding claim 1, in column 4 lines 50-67, FIG. 1 illustrates a Wireless Communication Device (WCD) comprises a signal processing module 108, which includes a modem 120 and a conventional SIM 122 adapted to be installed in (for example, plugged into) and removed (for example, un-plugged) from signal processing module 108. Modem 120 and SIM 122 correspond to the claimed first integrated circuit domain and second integrated circuit domain.

In column 5 lines 15-25, Hutchison, IV et al. teaches that modem 120 can control access the information contained in SIM 122 through SIM I/F 130. In column 6 lines 10-25, see also FIG. 2A, Hutchison, IV et al. further teaches that modem 120 derives a SIM reset signal ("SIM_RST") at a modem output labeled "RST" and transmits the SIM reset signal to SIM reset input 210 over a reset line 222. The SIM reset signal can be used to reset SIM 122. In light of that, the SIM reset signal corresponds to the claimed inter-ICD reset control signal.

Hutchison, IV et al. does not expressly teach the step of generating control signals in a first ICD as set forth in the application claim.

Shin teaches in another US patent a designed reset circuit for a modem operable in one of a basic modem mode for an exchange of digital data and an additional modem mode for an exchange of specific data in a computer system having an external reset switch provided thereon for allowing a user to manually reset operation of the modem.

The reset circuit generates a modem-reset signal to a reset portion of the modem for resetting operation of the modem; see column 2 lines 55-67. Furthermore, in column column 2 line 55 via column 3 line 15, Shin further teaches a reset circuit includes a decoder section for receiving information from the computer system and decoding the same information to generate a first reset signal; and a signal output section having a first input terminal coupled to receive occurrence of the first reset signal, and a second input terminal coupled to receive occurrence of a second reset signal generated from the computer system in response to one of manual depression of the external reset switch and execution of a designated program contained in the computer system, for combining occurrence of the first reset signal and the second reset signal to generate a modem reset signal to a reset portion of the modem for resetting operation of the modem. In view of the foregoing teachings, the reset signal resets the modem in a phased manner.

As disclosed in column 1 lines 15-20, because the designed reset circuit for a modem further includes a variety of additional functions such as facsimile function, voice function, or combined facsimile and voice function, one of ordinary skill in the art

at the time the invention was made would have been motivated to modify Hutchison, IV et al. teachings to implement a reset circuit for a modem as taught by Shin.

Regarding claim 2, in column 2 lines 55-67, Shin further teaches an external reset switch provided for allowing a user to manually reset operation of the modem.

Regarding claim 9, claim is rejected on the same ground as for claim 9 because of similar scope.

Regarding claim 10, claim is rejected on the same ground as for claim 2 because of similar scope.

Regarding claim 17, claim is rejected on the same ground as for claim 1 because of similar scope.

Allowable Subject Matter

2. Claims 3-8, 11-16 and 18-24 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Kim U.S. Patent 5,850,546 discloses "Central Processing Unit Reset Device And A Reset Method For A Central Processing Unit".

Reents et al. U.S. Patent 5,561,384 discloses "Input/Output Driver Circuit For Isolating With Minimal Power Consumption A Peripheral Component From A Core Section".

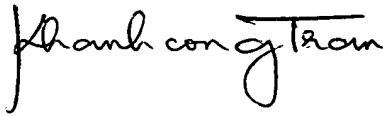
Takita et al. U.S. Patent 6,628,564 discloses "Semiconductor Memory Device Capable Of Driving Non-Selected Word Lines To First And Second Potentials".

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Khanh Tran whose telephone number is 571-272-3007. The examiner can normally be reached on Monday - Friday from 08:00 AM - 05:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jay Patel can be reached on 571-272-2988. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

KCT

 12/21/2006
Khanh Tran

Primary Examiner